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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/938,978	08/24/2001	Alan N. Willson JR.	G&C 31006.2-US-U1	2152
22462	7590	11/02/2005	EXAMINER	
GATES & COOPER LLP HOWARD HUGHES CENTER 6701 CENTER DRIVE WEST, SUITE 1050 LOS ANGELES, CA 90045			NGO, CHUONG D	
			ART UNIT	PAPER NUMBER
			2193	

DATE MAILED: 11/02/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

DETAILED ACTION

1. Claims 1-13,15,16,17-28,30,31 and 33-35 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

As per claim 1, the recitation "bypass input", lines 3, is indefinite as to what is bypassing and what is bypassed in accordance to the "bypass input". Claims 18,33,37 and 47 have the same problems. Further, the limitation of "to hold", line 5, is indefinite as whether it means to store or to stop from transmitting or both. According to the applicant's remark filed on 07/08/2005, "to hold a value" appears to means both that is to store the values and stop it from transmitting, this meaning is thus adopted for the purpose of examination. However, the claims should explicitly define the limitation. claims 15,16,18,30,31,33,37,45 also have the same problem.

2. Claims 14,17,29 and 32 stand rejected under 35 U.S.C. 102(b) as being clearly anticipated by Smith, Jr. (3,482,085).

Smith Jr. discloses in figure 1 an adder for adding a first input (E), a second input (P), and a carry input (F) to produce an adder output (T) and a carry out (G). The adder also has a bypass input (K) for controlling the logic of the adder to generate an adder output and a carry without computing as claimed (suppressing the arithmetic operations of the adder, see abstract and col. 5, lines 40-71).

3. Applicant's arguments filed 07/08/2005 have been fully considered but they are not persuasive with respect to claims 14,17,29,32 because Smith Jr. clearly discloses in the abstract the suppression of the arithmetic operations of the adder as claimed.
4. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chuong D. Ngo whose telephone number is (571) 272-3731. The examiner can normally be reached on Tuesday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kakali Chaki can be reached on (571) 272-3719. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Chuong D Ngo

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Primary Examiner
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10/31/2005